



Table of Contents	
2	Notes
3	Power, Clock, and Reset
4	User Functions
5	USB to BDM Reference Block

Revisions			
Rev	Description	Date	Approved
A	Original Release	03/30/05	DK
B	Updated per customer feedback and RoHS requirements	10/13/05	DK
C	Rotated U2	11/11/05	DK
D	No Pop'ed R25	03/01/06	DK
E	Change LED1 to 370-75102	09/18/07	DK
F	Change U5 to MMA6270QT	10/12/07	DK

		<b>Transportation &amp; Standard Products Group</b> <small>6501 William Cannon Drive West Austin, TX 78735-6598</small>	
<small>This document contains information proprietary to Freescale Semiconductor and shall not be used for engineering design, procurement or manufacture in whole or in part without the express written permission of Freescale Semiconductor.</small>			
<small>Designer:</small> DANIEL KRUCZEK	<small>Drawing Title:</small> <b>DEMO9S08AW60E</b>		
<small>Drawn by:</small> DANIEL KRUCZEK	<small>Page Title:</small> <b>TITLE PAGE</b>		
<small>Approved:</small> DANIEL KRUCZEK	<small>Size:</small> C	<small>Document Number:</small> PDF: SPF-20805 SOURCE: SCH-20805	<small>Rev:</small> F
<small>Date:</small> Monday, October 22, 2007		<small>Sheet 1 of 5</small>	

1. Unless Otherwise Specified:
  - All resistors are in ohms, 5%, 1/8 Watt
  - All capacitors are in uF, 20%, 50V
  - All voltages are DC
  - All polarized capacitors are aluminum electrolytic
2. Interrupted Lines coded with the same letter or letter combinations are electrically connected.
3. Device type number is for reference only. The number varies with the manufacturer.
4. Special signal usage:
  - \_B Denotes - Active-Low Signal
  - <> or [] Denotes - Vectored Signals
5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

		
Drawing Title: <b>DEMO9S08AW60E</b>		
Page Title: <b>NOTES</b>		
Size C	Document Number PDF: SPF-20805 SOURCE: SCH-20805	Rev F
Date: Monday, October 22, 2007	Sheet 2	of 5

